



**Universitas Negeri Surabaya  
Vocational Faculty,  
D4 Electrical Engineering Study Program**

Document Code

**SEMESTER LEARNING PLAN**

|  |   |   |                                   |  |                                    |  |                              |   |   |    |    |    |    |    |    |    |  |  |  |  |  |  |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |
|--|---|---|-----------------------------------|--|------------------------------------|--|------------------------------|---|---|----|----|----|----|----|----|----|--|--|--|--|--|--|---|---|---|---|---|---|---|---|---|----|----|----|----|----|----|----|
| <b>Courses</b>                         | <b>CODE</b>   | <b>Course Family</b>  | <b>Credit Weight</b>              | <b>SEMESTER</b>  | <b>Compilation Date</b>            |  |                              |   |   |    |    |    |    |    |    |    |  |  |  |  |  |  |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |
| DC Electrical Circuits                 | 204010229810  |   | T=0 P=0 ECTS=0                    | 1  | July 17, 2024                      |  |                              |   |   |    |    |    |    |    |    |    |  |  |  |  |  |  |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |
| <b>AUTHORIZATION</b>                   | <b>SP Developer</b>   |   | <b>Course Cluster Coordinator</b> |  | <b>Study Program Coordinator</b>   |  |                              |   |   |    |    |    |    |    |    |    |  |  |  |  |  |  |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |
|  | .....   |   | .....                             |  | Mahendra Widyartono,<br>S.T., M.T. |  |                              |   |   |    |    |    |    |    |    |    |  |  |  |  |  |  |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |
| <b>Learning model</b>                  | Project Based Learning  |   |                                   |  |                                    |  |                              |   |   |    |    |    |    |    |    |    |  |  |  |  |  |  |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |
| <b>Program Learning Outcomes (PLO)</b> | PLO study program that is charged to the course   |   |                                   |  |                                    |  |                              |   |   |    |    |    |    |    |    |    |  |  |  |  |  |  |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |
|  | Program Objectives (PO)   |   |                                   |  |                                    |  |                              |   |   |    |    |    |    |    |    |    |  |  |  |  |  |  |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |
|  | PLO-PO Matrix   |   |                                   |  |                                    |  |                              |   |   |    |    |    |    |    |    |    |  |  |  |  |  |  |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |
|  |   | <table border="1" style="margin: auto;"> <tr> <td style="width: 100px; height: 30px;">P.O</td> </tr> </table> |                                   |  |                                    |  | P.O                          |   |   |    |    |    |    |    |    |    |  |  |  |  |  |  |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |
| P.O                                    |   |   |                                   |  |                                    |  |                              |   |   |    |    |    |    |    |    |    |  |  |  |  |  |  |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |
|  | PO Matrix at the end of each learning stage (Sub-PO)  |   |                                   |  |                                    |  |                              |   |   |    |    |    |    |    |    |    |  |  |  |  |  |  |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |
|  | <table border="1" style="margin: auto;"> <tr> <td rowspan="2" style="width: 30px; height: 30px;">P.O</td> <td colspan="16" style="text-align: center;">Week</td> </tr> <tr> <td style="width: 20px;">1</td> <td style="width: 20px;">2</td> <td style="width: 20px;">3</td> <td style="width: 20px;">4</td> <td style="width: 20px;">5</td> <td style="width: 20px;">6</td> <td style="width: 20px;">7</td> <td style="width: 20px;">8</td> <td style="width: 20px;">9</td> <td style="width: 20px;">10</td> <td style="width: 20px;">11</td> <td style="width: 20px;">12</td> <td style="width: 20px;">13</td> <td style="width: 20px;">14</td> <td style="width: 20px;">15</td> <td style="width: 20px;">16</td> </tr> </table> |   |                                   |  |                                    | P.O                                      | Week                         |   |   |    |    |    |    |    |    |    |  |  |  |  |  |  | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 |
| P.O                                    | Week  |   |                                   |  |                                    |  |                              |   |   |    |    |    |    |    |    |    |  |  |  |  |  |  |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |
|  | 1   | 2   | 3                                 | 4  | 5                                  | 6  | 7                            | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 |  |  |  |  |  |  |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |
| <b>Short Course Description</b>        | Understanding and study of: (1) atomic theory, knowledge and circuit parameters, (2) basic laws of electricity and basic theory of electrical circuits, (3) direct current electric power (4) mesh current analysis, (mesh current analysis), (5) node voltage analysis, (6) resistance network analysis presented in theoretical form and problem solving.   |   |                                   |  |                                    |  |                              |   |   |    |    |    |    |    |    |    |  |  |  |  |  |  |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |
| <b>References</b>                      | <b>Main :</b>   |   |                                   |  |                                    |  |                              |   |   |    |    |    |    |    |    |    |  |  |  |  |  |  |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |
|  | <ol style="list-style-type: none"> <li>1. Boylestad, Robert L., 2007. Introductory Circuit Analysis -11th ed . New Jersey; Pearson Prentice Hall</li> <li>2. Floyd, 2007. Electric Circuits Fundamentals 13 7th ed. New Jersey; Pearson Prentice Hall</li> <li>3. William, Jack, &amp; Steven. 2005. Engineering Circuit Analysis Sixth Edition (diterjemahkan oleh Wiwit Kastawan) . Jakarta:Erlangga.</li> <li>4. Ramdhani, Mohamad. 2008. Rangkaian Listrik . Jakarta: Erlangga</li> <li>5. dll</li> </ol>   |   |                                   |  |                                    |  |                              |   |   |    |    |    |    |    |    |    |  |  |  |  |  |  |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |
|  | <b>Supporters:</b>  |   |                                   |  |                                    |  |                              |   |   |    |    |    |    |    |    |    |  |  |  |  |  |  |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |
| <b>Supporting lecturer</b>             | Reza Rahmadian, S.ST., M.EngSc.   |   |                                   |  |                                    |  |                              |   |   |    |    |    |    |    |    |    |  |  |  |  |  |  |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |
| <b>Week-</b>                           | <b>Final abilities of each learning stage (Sub-PO)</b>  | <b>Evaluation</b>   |                                   | <b>Help Learning, Learning methods, Student Assignments, [ Estimated time]</b> |                                    | <b>Learning materials [ References ]</b> | <b>Assessment Weight (%)</b> |   |   |    |    |    |    |    |    |    |  |  |  |  |  |  |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |
|  |   | <b>Indicator</b>  | <b>Criteria &amp; Form</b>        | <b>Offline ( offline )</b>   | <b>Online ( online )</b>           |  |                              |   |   |    |    |    |    |    |    |    |  |  |  |  |  |  |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |
| (1)                                    | (2)   | (3)   | (4)                               | (5)  | (6)                                | (7)                                      | (8)                          |   |   |    |    |    |    |    |    |    |  |  |  |  |  |  |   |   |   |   |   |   |   |   |   |    |    |    |    |    |    |    |

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|---|--|--|--|--|--|----|
| 1 | Describe, give examples and apply atomic theory, basic knowledge concepts and circuit parameters | <ol style="list-style-type: none"> <li>1.Explain about atomic theory,</li> <li>2.Explain the meaning of electron flow</li> <li>3.Explain the meaning of electric current</li> <li>4.Explain the meaning of electric potential</li> <li>5.Explain the meaning of voltage/voltage difference</li> <li>6.Explain the meaning of electrical units</li> <li>7.Explain the meaning of electric charge</li> <li>8.Explain the meaning of capacitance</li> <li>9.Calculating conductor resistance</li> <li>10.Calculate changes in resistance due to changes in temperature</li> </ol> | <b>Criteria:</b><br>The correct answer gets a score of 100 | Discussion, providing examples of application and assignments in the 2 X 50 theory class |  | 0% |
| 2 | Describe, give examples and apply atomic theory, basic knowledge concepts and circuit parameters | <ol style="list-style-type: none"> <li>1.Explain about atomic theory,</li> <li>2.Explain the meaning of electron flow</li> <li>3.Explain the meaning of electric current</li> <li>4.Explain the meaning of electric potential</li> <li>5.Explain the meaning of voltage/voltage difference</li> <li>6.Explain the meaning of electrical units</li> <li>7.Explain the meaning of electric charge</li> <li>8.Explain the meaning of capacitance</li> <li>9.Calculating conductor resistance</li> <li>10.Calculate changes in resistance due to changes in temperature</li> </ol> | <b>Criteria:</b><br>The correct answer gets a score of 100 | Discussion, providing examples of application and assignments in the 2 X 50 theory class |  | 0% |

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| 3 | Understand and apply the basic laws of electricity and basic theory of electrical circuits | <ol style="list-style-type: none"> <li>1.Explain direct current (DC) generation</li> <li>2.Explain the types of direct current</li> <li>3.Explain Faraday's law</li> <li>4.Explain Kirchhoff's law 19s</li> <li>5.Explain Ohm's law</li> <li>6.Explain Lenz's law</li> <li>7.Calculate the branch voltage across some resistance</li> <li>8.Calculate the equivalent resistance in a series circuit.</li> <li>9.Calculating equivalent resistance in parallel circuits.</li> <li>10.Calculating the branch current in a two-branch parallel circuit.</li> <li>11.Calculating equivalent resistance in series-parallel (mixed) circuits</li> <li>12.Calculate the magnitude of the conductance G</li> </ol> | <p><b>Criteria:</b><br/>test score: number of correct answers x 100, divided by the number of test items</p> | Discussion, giving examples of R series problems and assignments in theory class, 2 X 50 |  | 0% |
|---|--|--|--|--|--|----|

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| 4 | Understand and apply the basic laws of electricity and basic theory of electrical circuits            | <ol style="list-style-type: none"> <li>1.Explain direct current (DC) generation</li> <li>2.Explain the types of direct current</li> <li>3.Explain Faraday's law</li> <li>4.Explain Kirchhoff's law 19s</li> <li>5.Explain Ohm's law</li> <li>6.Explain Lenz's law</li> <li>7.Calculate the branch voltage across some resistance</li> <li>8.Calculate the equivalent resistance in a series circuit.</li> <li>9.Calculating equivalent resistance in parallel circuits.</li> <li>10.Calculating the branch current in a two-branch parallel circuit.</li> <li>11.Calculating equivalent resistance in series-parallel (mixed) circuits</li> <li>12.Calculate the magnitude of the conductance G</li> </ol> | <b>Criteria:</b><br>test score: number of correct answers x 100, divided by the number of test items                        | Discussion, giving examples of R series problems and assignments in theory class, 2 X 50  |  |  | 0% |
| 5 | Can analyze and evaluate the concept of direct current electric power, and practice in the laboratory | <ol style="list-style-type: none"> <li>1. Calculate the amount of DC2 electrical power. calculate DC3 electrical work. calculate DC4 electric heat. Skilled in carrying out practical work in the laboratory to validate electrical power.</li> </ol>  | <b>Criteria:</b><br>The test score is obtained by: number of correct answers x 100 then divided by the number of test items | Discussion, providing examples of electrical power problems and assignments in theory classes. 2 X 50   |  |  | 0% |
| 6 | Able to use the mesh current method to solve problems in complex direct current circuits              | <ol style="list-style-type: none"> <li>1. Calculate the number of mesh currents, 2. Determine the direction of the mesh current, 3. Write the mesh current equation 4. Calculate the magnitude of each mesh current using elimination 5. Calculate the magnitude of each mesh current using a matrix.6. Calculate the amount of current, voltage or resistance in the mesh using driving point resistance7. Calculate the amount of current, voltage, or resistance in the mesh using transfer resistance</li> </ol>   | <b>Criteria:</b><br>The test score is obtained by: number of correct answers x 100 then divided by the number of test items | Discussion, providing examples of solving complex electrical circuits using the mesh current method and assignments in theory classes. 2 X 50 |  |  | 0% |

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| 7  | Able to use the mesh current method to solve problems in complex direct current circuits            | 1. Calculate the number of mesh currents, 2. Determine the direction of the mesh current, 3. Write the mesh current equation 4. Calculate the magnitude of each mesh current using elimination 5. Calculate the magnitude of each mesh current using a matrix.6. Calculate the amount of current, voltage or resistance in the mesh using driving point resistance7. Calculate the amount of current, voltage, or resistance in the mesh using transfer resistance | <b>Criteria:</b><br>The test score is obtained by:<br>number of correct answers x 100 then divided by the number of test items | Discussion, providing examples of solving complex electrical circuits using the mesh current method and assignments in theory classes.<br>2 X 50                 |  |  | 0% |
| 8  | UTS   |  |  | 2 X 50   |  |  | 0% |
| 9  | Able to use the Branch Current analysis method to solve problems in direct current circuits         | 1.Determines the number of branching currents<br>2.Determine the number of equations   | <b>Criteria:</b><br>The test score is obtained by:<br>number of correct answers x 100 then divided by the number of test items | Discussion, providing examples of solving electrical circuits using the Branch Current analysis method and assignments in theory classes.<br>2 X 50              |  |  | 0% |
| 10 | Able to use Node analysis methods to solve problems in direct current circuits                      | 1.Defines the reference node as ground/zero potential<br>2.Determines node voltage<br>3.Determines the direction of current leaving the node<br>4.Determine the number of equations  | <b>Criteria:</b><br>The test score is obtained by:<br>number of correct answers x 100 then divided by the number of test items | Discussion, providing examples of solving electrical circuits using Node analysis methods and assignments in theory classes.<br>2 X 50                           |  |  | 0% |
| 11 | Able to use Node analysis methods to solve problems in direct current circuits                      | 1.Defines the reference node as ground/zero potential<br>2.Determines node voltage<br>3.Determines the direction of current leaving the node<br>4.Determine the number of equations  | <b>Criteria:</b><br>The test score is obtained by:<br>number of correct answers x 100 then divided by the number of test items | Discussion, providing examples of solving electrical circuits using Node analysis methods and assignments in theory classes.<br>2 X 50                           |  |  | 0% |
| 12 | Able to use the Superposition and Substitution theorem to solve problems in direct current circuits | 1.Determine the equivalent circuit by replacing the current source with an internal resistance<br>2.Determine the equivalent circuit by replacing the voltage source with an internal resistance   | <b>Criteria:</b><br>The test score is obtained by:<br>number of correct answers x 100 then divided by the number of test items | Discussion, providing examples of solving electrical circuits using the Superposition and Substitution theorem as well as assignments in theory class.<br>2 X 50 |  |  | 0% |

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| 13 | Able to use Thevenin's theorem to solve problems in direct current circuits                           | 1.Determine the short circuit current value<br>2.Determine the replacement resistance value                                      | <b>Criteria:</b><br>The test score is obtained by:<br>number of correct answers x 100 then divided by the number of test items | Discussion, providing examples of solving electrical circuits using Thevenin's theorem as well as assignments in theory class.<br>2 X 50                           |  | 0% |
| 14 | Able to use Norton's theorem to solve problems in direct current circuits                             | 1.Determine the terminal point<br>2.Determines the current value at the terminal<br>3.Determine the replacement resistance value | <b>Criteria:</b><br>The test score is obtained by:<br>number of correct answers x 100 then divided by the number of test items | Discussion, providing examples of solving electrical circuits using Norton's theorem as well as assignments in theory class.<br>2 X 50                             |  | 0% |
| 15 | Able to use Millman's theorem and Maximum Power Transfer to solve problems in direct current circuits | 1.Determine the number of current sources<br>2.Determines the maximum power value  | <b>Criteria:</b><br>The test score is obtained by:<br>number of correct answers x 100 then divided by the number of test items | Discussion, providing examples of solving electrical circuits using Millman's theorem and Maximum Power Transfer as well as assignments in theory class.<br>2 X 50 |  | 0% |
| 16 | UAS   |  |  | 2 X 50   |  | 0% |

#### Evaluation Percentage Recap: Project Based Learning

| No | Evaluation | Percentage |
|----|------------|------------|
|    |            | 0%         |

#### Notes

- Learning Outcomes of Study Program Graduates (PLO - Study Program)** are the abilities possessed by each Study Program graduate which are the internalization of attitudes, mastery of knowledge and skills according to the level of their study program obtained through the learning process.
- The PLO imposed on courses** are several learning outcomes of study program graduates (CPL-Study Program) which are used for the formation/development of a course consisting of aspects of attitude, general skills, special skills and knowledge.
- Program Objectives (PO)** are abilities that are specifically described from the PLO assigned to a course, and are specific to the study material or learning materials for that course.
- Subject Sub-PO (Sub-PO)** is a capability that is specifically described from the PO that can be measured or observed and is the final ability that is planned at each learning stage, and is specific to the learning material of the course.
- Indicators for assessing** abilities in the process and student learning outcomes are specific and measurable statements that identify the abilities or performance of student learning outcomes accompanied by evidence.
- Assessment Criteria** are benchmarks used as a measure or measure of learning achievement in assessments based on predetermined indicators. Assessment criteria are guidelines for assessors so that assessments are consistent and unbiased. Criteria can be quantitative or qualitative.
- Forms of assessment:** test and non-test.
- Forms of learning:** Lecture, Response, Tutorial, Seminar or equivalent, Practicum, Studio Practice, Workshop Practice, Field Practice, Research, Community Service and/or other equivalent forms of learning.
- Learning Methods:** Small Group Discussion, Role-Play & Simulation, Discovery Learning, Self-Directed Learning, Cooperative Learning, Collaborative Learning, Contextual Learning, Project Based Learning, and other equivalent methods.
- Learning materials** are details or descriptions of study materials which can be presented in the form of several main points and sub-topics.
- The assessment weight** is the percentage of assessment of each sub-PO achievement whose size is proportional to the level of difficulty of achieving that sub-PO, and the total is 100%.
- TM=Face to face, PT=Structured assignments, BM=Independent study.

