

		Universitas Negeri Surabaya Vocational Faculty, D4 Electrical Engineering Study Program					Document Code																																										
SEMESTER LEARNING PLAN																																																	
Courses		CODE	Course Family	Credit Weight			SEMESTER	Compilation Date																																									
Analog and Digital Electronics Practice		99992040102031		T=2	P=0	ECTS=3.18	2	July 17, 2024																																									
AUTHORIZATION		SP Developer		Course Cluster Coordinator			Study Program Coordinator																																										
				Mahendra Widyartono, S.T., M.T.																																										
Learning model	Project Based Learning																																																
Program Learning Outcomes (PLO)	PLO study program that is charged to the course																																																
	Program Objectives (PO)																																																
	PLO-PO Matrix																																																
		<table border="1" style="margin-left: auto; margin-right: auto;"> <tr> <td style="width: 100px; height: 30px;">P.O</td> </tr> </table>							P.O																																								
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PO Matrix at the end of each learning stage (Sub-PO)																																																	
	<table border="1" style="margin-left: auto; margin-right: auto;"> <tr> <td rowspan="2" style="width: 30px; height: 30px;">P.O</td> <td colspan="16" style="text-align: center;">Week</td> </tr> <tr> <td style="width: 20px;">1</td> <td style="width: 20px;">2</td> <td style="width: 20px;">3</td> <td style="width: 20px;">4</td> <td style="width: 20px;">5</td> <td style="width: 20px;">6</td> <td style="width: 20px;">7</td> <td style="width: 20px;">8</td> <td style="width: 20px;">9</td> <td style="width: 20px;">10</td> <td style="width: 20px;">11</td> <td style="width: 20px;">12</td> <td style="width: 20px;">13</td> <td style="width: 20px;">14</td> <td style="width: 20px;">15</td> <td style="width: 20px;">16</td> </tr> </table>																P.O	Week																1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
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Short Course Description	This course teaches about analog and digital systems, diode characteristics, transistor characteristics, basic logic gates, combinational circuits and sequential circuits, the use of Boolean algebra and Karnaugh maps in simplifying circuits, adders including half-adder and full-adder, as well as flip-flops which are memory cell formation circuit																																																
References	Main :																																																
	Supporters:																																																
Supporting lecturer	Reza Rahmadian, S.ST., M.EngSc. Ayusta Lukita Wardani, S.ST., M.T.																																																
Week-	Final abilities of each learning stage (Sub-PO)	Evaluation		Help Learning, Learning methods, Student Assignments, [Estimated time]		Learning materials [References]	Assessment Weight (%)																																										
		Indicator	Criteria & Form	Offline (offline)	Online (online)																																												
(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)																																										
1	-	-	Criteria: -	- 2 X 50			0%																																										
2	-	-	Criteria: -	- 2 X 50			0%																																										
3	-	-	Criteria: -	- 2 X 50			0%																																										

4	-	-	Criteria: -	- 2 X 50			0%
5	-	-	Criteria: -	- 2 X 50			0%
6	-	-	Criteria: -	- 2 X 50			0%
7	-	-	Criteria: -	- 2 X 50			0%
8	-	-	Criteria: -	- 2 X 50			0%
9	-	-	Criteria: -	- 2 X 50			0%
10	-	-	Criteria: -	- 2 X 50			0%
11	-	-	Criteria: -	- 2 X 50			0%
12	-	-	Criteria: -	- 2 X 50			0%
13	-	-	Criteria: -	- 2 X 50			0%
14	-	-	Criteria: -	- 2 X 50			0%
15	-	-	Criteria: -	- 2 X 50			0%
16	-	-	Criteria: -	- 2 X 50			0%

Evaluation Percentage Recap: Project Based Learning

No	Evaluation	Percentage
		0%

Notes

- Learning Outcomes of Study Program Graduates (PLO - Study Program)** are the abilities possessed by each Study Program graduate which are the internalization of attitudes, mastery of knowledge and skills according to the level of their study program obtained through the learning process.
- The PLO imposed on courses** are several learning outcomes of study program graduates (CPL-Study Program) which are used for the formation/development of a course consisting of aspects of attitude, general skills, special skills and knowledge.
- Program Objectives (PO)** are abilities that are specifically described from the PLO assigned to a course, and are specific to the study material or learning materials for that course.
- Subject Sub-PO (Sub-PO)** is a capability that is specifically described from the PO that can be measured or observed and is the final ability that is planned at each learning stage, and is specific to the learning material of the course.
- Indicators for assessing** ability in the process and student learning outcomes are specific and measurable statements that identify the ability or performance of student learning outcomes accompanied by evidence.
- Assessment Criteria** are benchmarks used as a measure or measure of learning achievement in assessments based on predetermined indicators. Assessment criteria are guidelines for assessors so that assessments are consistent and unbiased. Criteria can be quantitative or qualitative.
- Forms of assessment:** test and non-test.
- Forms of learning:** Lecture, Response, Tutorial, Seminar or equivalent, Practicum, Studio Practice, Workshop Practice, Field Practice, Research, Community Service and/or other equivalent forms of learning.
- Learning Methods:** Small Group Discussion, Role-Play & Simulation, Discovery Learning, Self-Directed Learning, Cooperative Learning, Collaborative Learning, Contextual Learning, Project Based Learning, and other equivalent methods.
- Learning materials** are details or descriptions of study materials which can be presented in the form of several main points and sub-topics.
- The assessment weight** is the percentage of assessment of each sub-PO achievement whose size is proportional to the level of difficulty of achieving that sub-PO, and the total is 100%.
- TM=Face to face, PT=Structured assignments, BM=Independent study.

