



Universitas Negeri Surabaya Faculty of Engineering, Mechanical Engineering Undergraduate Study Program

			SEN	ΛE	ST	ER	LE	ΕΑΙ	RN	INC	G P	L <i>F</i>	NA							
Courses			CODE			C	Cours	se Fai	mily		Cr	edit	Weig	ght	s	EMES	TER	Cor	mpilati e	on
Instrumentati	on and Control		2120102028	3				rengtl Progi			T=	2 F	P=0	ECTS=3.	18	6	6	Jan 202	uary 2 !3	3,
AUTHORIZAT	TION		SP Develop	er		•				Cour	se Clu	ster	r Coc	ordinator	s	tudy F	rogra	m Cod	rdinat	or
			Wahyu Dwi Agung Prijo					.Pd.;		Agun M.T.	g Prijo	Buc	dijono	o, S.T.,	lr	. Priyo		Adiwib 1.T.	owo, S	i.T.,
Learning model	Project Based L	earnin	g												•					
Program	PLO study pro	gram t	that is char	ged t	o the	cou	ırse													
Learning Outcomes	PLO-5	Work	independent	ly and	d in g	roups	6													
(PLO)	PLO-14	Science and engineering knowledge																		
	Program Object	Objectives (PO)																		
	PO - 1	Have	good morals	, ethic	cs and	d pers	sonal	ity in s	study	ing in	strum	enta	tion a	and contro	ol syst	ems				
	PO - 2	Have produ	knowledge o	f the	basic	princ	iples	of ins	strum	entati	ion and	d co	ntrol	systems (on ind	ustrial	machi	nes to	produc	е а
	PO - 3	Able t	to collaborate	and	be re	spons	sible	in dev	elopi	ing in	strume	ntat	tion a	ınd contro	l syst	ems ac	cordin	g to ne	eeds	
	PO - 4	Have	the ability to	desig	n inst	trume	entatio	on and	d cor	ntrol s	ystem	for	indu	strial mad	hines	to pro	duce a	produ	ıct	
	PLO-PO Matrix																			
											_									
			P.O		PL	.O-5		F	PLO-	14										
			PO-1																	
			PO-2																	
			PO-3																	
			PO-4																	
	PO Matrix at th	e end	of each lea	rning	ısta	ae (S	Sub-F	20)												
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			P.O									We	ek]
				1	2	3	4	5	6	7	8	9	10) 11	12	13	14	15	16	
		PC	D-1	_	_		·				Ü					10			10	
		PC	D-2																	
		PC	D-3																	
		PC	D-4																	
								1									ı			ı
Short Course Description	In this course stu types of sensors, learning in the fo case studies, and	basic rm of I	logic gates, lectures, prac	Boole cticum	an al	gebra	a, rela	ay cor	ntrol a	and p	rogran	ıma	ble lo	ogic contr	ollers	(PLC)	using	variou	s form	s of
References	Main :																			
	1. Dunn, W	illiam C	C. 2005. Fund	damei	ntals (of Ind	lustria	al Istru	umen	itation	and F	roce	ess C	Control. U	SA: M	lc Grav	v-Hill C	Compa	nies, Ir	IC.
	Supporters:																			

- 1. Bolton, W. 2006. Sistem Instrumentasi dan Sistem Kontrol. Penerbit Erlangga: Jakarta
- 2. Groover, Mikell P., 2001. Automation, Production Systems dan Computer Integrated Manufacturing, Second Edition, Prentice-Hall Inc., New Jersey USA.

 3. Johnson, C.D. 2003. Process Control Instrumentation Technology, Seventh Edition. USA: Prentice Hall Inc., New Jersey.

Agung Prijo Budijono, S.T., M.T. Ir. Wahyu Dwi Kurniawan, S.Pd., M.Pd. Supporting lecturer

Week-	Final abilities of each learning stage	Eva	lluation	Learni Student	Learning, ing methods, Assignments, imated time]	Learning materials	Assessment Weight (%)	
	(Sub-PO)	Indicator	Criteria & Form	Offline (offline)	Online (online)	- [References]		
(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	
1	Able to explain basic principles and draw block diagrams of instrumentation and control systems	Accuracy explains the basic principles of instrumentation and control systems	Criteria: Assessment rubric Form of Assessment: Participatory Activities, Tests	* Lectures, * Case studies, discussions in groups * 2x50		Material: Basic principles and drawing block diagrams of instrumentation and control systems. Reference: Dunn, William C. 2005. Fundamentals of Industrial Instrumentation and Process Control. USA: Mc Graw-Hill Companies, Inc.	2%	
2	Able to explain basic principles and draw block diagrams of instrumentation and control systems	Designing a block diagram of a control system	Criteria: Assessment rubric Form of Assessment: Participatory Activities, Tests	* Lectures, * Case studies, discussions in groups * Assignment- 1: Draw block diagrams of open loop and close loop control systems * 2x50		Material: Basic principles and drawing block diagrams of instrumentation and control systems. Reference: Dunn, William C. 2005. Fundamentals of Industrial Instrumentation and Process Control. USA: Mc Graw-Hill Companies, Inc.	2%	
3	Students are able to identify various types of sensors and their applications	Accuracy of identifying at least 5 types of sensors	Criteria: Assessment rubric Form of Assessment: Participatory Activities	* Lectures, * Discovery learning, discussions in groups * 2x50		Material: Various types of sensors and their applications References: Dunn, William C. 2005. Fundamentals of Industrial Instrumentation and Process Control. USA: Mc Graw-Hill Companies, Inc.	2%	
4	Able to identify various types of sensors and their applications	Accuracy explains the application of at least 5 types of sensors	Criteria: Assessment rubric Form of Assessment : Participatory Activities, Tests	* Lecture, * Discovery learning, * Discussion in groups, * Task-2: Identify at least 5 types of sensors with their applications * 2x50		Material: Various types of sensors and their applications References: Dunn, William C. 2005. Fundamentals of Industrial Instrumentation and Process Control. USA: Mc Graw-Hill Companies, Inc.	2%	

5 Distinguish working pri of basic log	inciples distinguishes	Criteria: Assessment rubric Forms of Assessment : Participatory Activities, Portfolio Assessment, Tests	* Lecture, * Discussion in groups, * Assignment- 3: Explain the different working principles of AND, OR, NOT, NAND, NOR logic gates * 2x50	Material: Basic logic gates Reference: Bolton, W. 2006. Instrumentation Systems and Control Systems. Erlangga Publisher: Jakarta	5%
6 Able to app Boolean all simplify log equations	gebra to of applying	Criteria: Compliance with the assessment rubric Form of Assessment: Participatory Activities	* Lectures, * Discovery learning, discussions in groups * 2x50	Material: Boolean Algebra Reference: Bolton, W. 2006. Instrumentation Systems and Control Systems. Erlangga Publisher: Jakarta	5%
7 Able to app Boolean all simplify log equations	gebra to of applying	Form of Assessment : Participatory Activities, Tests	* Lecture, * Discovery learning, discussion in groups, * Task-4: Simplify logical equations and describe them, * 2x50	Material: Boolean Algebra Reference: Bolton, W. 2006. Instrumentation Systems and Control Systems. Erlangga Publisher: Jakarta	5%
8 UTS	Compliance with the answer key gets a score of 100	Criteria: Compliance with the answer key gets a score of 100	UTS * 2x50		20%
9 Understand function, m parts, and principles of	nain explains the working function of the	Criteria: Assessment rubric Form of Assessment : Participatory Activities, Tests	* Lecture, * Discovery Learning, Discussion in groups, * Assignment- 5: Explain the function, main parts, and working principles of relays * 2x50	Material: Relay Bibliography: Groover, Mikell P., 2001. Automation, Production Systems and Computer Integrated Manufacturing, Second Edition, Prentice-Hall Inc., New Jersey USA.	10%
10 Understand logic gates		Criteria: Assessment rubric Forms of Assessment : Participatory Activities, Project Results Assessment / Product Assessment, Tests	Laboratory Practice Project Based Learning, Discussion in groups Phase 1: Determining Basic Questions The lecturer asks: What is the control sequence for the quiz quiz? Students respond to the lecturer's questions. Phase 2: Developing a project plan. The lecturer gives students time to design a series of quiz controls. Students design a series of quiz controls	Material: Design of relay control systems References: Groover, Mikell P., 2001. Automation, Production Systems and Computer Integrated Manufacturing, Second Edition, Prentice-Hall Inc., New Jersey USA.	20%

				for 3 participants in groups. • Phase 3: Develop a schedule. The lecturer makes an agreement on the deadline for submitting the project. Students develop a timeline for completing the project, a series of quizzes for 3 participants in groups • Phase 4: Monitoring The lecturer monitors the student process of collecting project results. Students submit their work according to the agreed time limit • Phase 5: Testing the Results Students test their circuits through computer simulations and relay trainer kits. The circuit testing process is observed by the lecturer to see the quality of the product. • Phase 6: Evaluation of Experience Students revise if the sequence is not correct Lecturer gives students time to reflect and revise the project Lecturer provides and input on the project * 2 X 50		
11	Understand the functions, main parts, advantages and disadvantages and programming procedures of PLC	Accuracy in explaining PL functions, the main parts of a PLC, identifying the advantages and disadvantages of a PLC, and understanding PLC programming procedures	Criteria: Assessment rubric Form of Assessment : Participatory Activities, Tests	* Lecture, * Discussion in groups, * Task-6: explain the function, main parts, advantages and disadvantages of PLC, * 2 X 50	Material: PLC Reference: Johnson, CD 2003. Process Control Instrumentation Technology, Seventh Edition. USA: Prentice Hall Inc., New Jersey.	5%
12	Designing PLC programs	Assessment rubric	Criteria: The accuracy of designing ladder diagrams using the Cx application. Programmer in completing the project Form of Assessment: Participatory	Laboratory Practice Project Based Learning, Discussion in groups Phase 1: Determining Basic Questions	Material: PLC Programming Reference: Johnson, CD 2003. Process Control Instrumentation Technology, Seventh Edition. USA: Prentice Hall	10%

			Activities, Project Results Assessment / Product Assessment	The lecturer asks: How to make a traffic light control circuit using a PLC? Students respond to the lecturer's questions. • Phase 2: Develop a project plan. The lecturer gives students time to design the program and operate the PLC. Students design a ladder diagram using cx.programmer in groups. • Phase 3: Develop a schedule . The lecturer makes an agreement on the deadline for submitting the project. Students develop a timeline for completing the PLC programming and operating project • Phase 4: Monitoring The lecturer monitors the student process of collecting project results. Students submit their work according to the agreed time limit • Phase 5: Testing the Results Students test their circuits using computer simulations and PLC trainer kits. The circuit testing process is observed by the lecturer to see the quality of the product. • Phase 6: Evaluation of Experience Students revise if the sequence is end correct Lecturer gives students trevise if the sequence is not correct Lecturer gives students revise if the sequence is not correct Lecturer gives students may be revised to reflect and revise the product. • Phase 6: Evaluation of Experience Students revise if the sequence is not correct Lecturer gives students time to reflect and revise the product. • Phase 6: Evaluation of Experience Students revise if the sequence is not correct to see the quality of the product. • Phase 6: Evaluation of Experience Students revise if the sequence is not product. • Phase 6: Evaluation of Experience Students revise if the sequence is not product. • Phase 6: Evaluation of Experience Students revise if the sequence is not product.	Inc., New Jersey.	
13	Designing PLC programs	The accuracy of designing ladder diagrams using the Cx	Criteria: Assessment rubric Form of	Laboratory PracticeProject Based	Material: PLC Programming Reference: Johnson, CD	10%

application. Programmer in	Assessment : Participatory	Learning, Discussion in	2003. Process Control
completing the	Activities	groups	Instrumentation
project	7101111100	• Phase 1:	Technology,
		Determining	Seventh
		Basic	Edition. USA:
		Questions	Prentice Hall
		The lecturer	Inc., New
		asks: How to	Jersey.
		make a traffic	
		light control	
		circuit using a	
		PLC?	
		Students	
		respond to the	
		lecturer's	
		questions.	
		• Phase 2:	
		Develop a	
		project plan.	
		The lecturer	
		gives students	
		time to design	
		the program	
		and operate	
		the PLC.	
		Students	
1		design a	
		ladder diagram	
1		using	
1		cx.programmer in groups.	
		• Phase 3:	
1		• Phase 3: Develop a	
		schedule	
1		. The lecturer	
1		makes an	
		agreement on	
		the deadline	
		for submitting	
		the project.	
		Students	
		develop a	
		timeline for	
		completing the	
		PLC	
		programming	
		and operating	
1		project	
		Phase 4: Manitaring	
1		Monitoring	
		The lecturer monitors the	
		student	
		process of collecting	
		project results.	
		Students	
		submit their	
		work according	
		to the agreed	
		time limit	
		• Phase 5:	
1		Testing the	
		Results	
1		Students test	
		their circuits	
1		using	
		computer	
1		simulations	
		and PLC	
1		trainer kits.	
		The circuit	
1		testing process	
		is observed by	
1		the lecturer to see the quality	
		of the product.	
		• Phase 6:	
		Evaluation of	
		Experience	
		Students	
		revise if the	
1		sequence is	
		not correct	
		Lecturer gives	
		students time	
		to reflect and	
		revise the	
		project	
	1		
		Lecturer	
		Lecturer provides	
		provides suggestions	

	O Hardin DLO	Ol West to		the project * 2 X 50		
14	Operate the PLC	Skilled in operating PLC	Criteria: Assessment rubric	* 2 X 50 • Laboratory Practice	Material: PLC Operation	10%
		according to	7.00000	Project	Reference:	
		procedures	Form of	Based	Johnson, CD	
			Assessment : Participatory	Learning,	2003. Process	
			Activities, Project	Discussion in groups	Control Instrumentation	
			Results Assessment	• Phase 1:	Technology,	
			/ Product	Determining	Seventh	
			Assessment	Basic Questions	Edition. USA: Prentice Hall	
				The lecturer	Inc., New	
				asks: How to	Jersey.	
				make a traffic light control		
				circuit using a		
				PLC?		
				Students respond to the		
				lecturer's		
				questions.		
				Phase 2: Develop a		
				Develop a project plan.		
				The lecturer		
				gives students		
				time to design the program		
				and operate		
				the PLC.		
				Students design a		
				ladder diagram		
				using		
				cx.programmer in groups.		
				• Phase 3:		
				Develop a		
				schedule . The lecturer		
				makes an		
				agreement on		
				the deadline for submitting		
				the project.		
				Students		
				develop a timeline for		
				completing the		
				PLC		
				programming and operating		
				project		
				Phase 4: Monitoring		
				The lecturer		
				monitors the		
				student process of		
				collecting		
				project results.		
				Students submit their		
				work according		
				to the agreed		
				time limit • Phase 5:		
				Testing the		
				Results		
				Students test their circuits		
				using		
				computer		
				simulations and PLC		
				and PLC trainer kits.		
				The circuit		
				testing process		
				is observed by the lecturer to		
				see the quality		
				of the product.		
				Phase 6: Evaluation of		
				Experience		
				Students		
				revise if the sequence is		
				not correct		
				Lecturer gives		
				students time		

15	Skilled in	Critorio	to reflect and revise the project Lecturer provides suggestions and input on the project * 2 X 50	Material: PLC	1004
15	Skilled in operating PLC according to procedures	Criteria: Assessment rubric Form of Assessment : Participatory Activities	Laboratory Practice Project Based Learning, Discussion in groups Phase 1: Determining Basic Questions The lecturer asks: How to make a traffic light control circuit using a PLC? Students respond to the lecturer's questions. Phase 2: Develop a project plan. The lecturer gives students time to design the program and operate the PLC. Students design a ladder diagram using cx.programmer in groups. Phase 3: Develop a schedule . The lecturer makes an agreement on the deadline for submitting the project. Students develop a timeline for completing the PLC programming and operating project Phase 4: Monitoring The lecturer monitors the student process of collecting project results. Students submit their work according to the agreed time limit Phase 5: Testing the Results Students test their circuits using computer simulations and PLC trainer kits. The circuit testing process is observed to see the quality of the product. Phase 6:	Material: PLC Operation Reference: Johnson, CD 2003. Process Control Instrumentation Technology, Seventh Edition. USA: Prentice Hall Inc., New Jersey.	10%

				Evaluation of Experience Students revise if the sequence is not correct Lecturer gives students time to reflect and revise the project Lecturer provides suggestions and input on the project *2 × 50		
16	UAS	Compliance with the answer key gets a score of 100	Criteria: Compliance with the answer key gets a score of 100	UAS * 2x50		30%

Evaluation Percentage Recap: Project Based Learning

No	Evaluation	Percentage
1.	Participatory Activities	58.34%
2.	Project Results Assessment / Product Assessment	16.67%
3.	Portfolio Assessment	1.67%
4.	Test	21.34%
		98.02%

Notes

- Learning Outcomes of Study Program Graduates (PLO Study Program) are the abilities possessed by each Study
 Program graduate which are the internalization of attitudes, mastery of knowledge and skills according to the level of their
 study program obtained through the learning process.
- The PLO imposed on courses are several learning outcomes of study program graduates (CPL-Study Program) which
 are used for the formation/development of a course consisting of aspects of attitude, general skills, special skills and
 knowledge.
- 3. **Program Objectives (PO)** are abilities that are specifically described from the PLO assigned to a course, and are specific to the study material or learning materials for that course.
- 4. **Subject Sub-PO (Sub-PO)** is a capability that is specifically described from the PO that can be measured or observed and is the final ability that is planned at each learning stage, and is specific to the learning material of the course.
- Indicators for assessing ability in the process and student learning outcomes are specific and measurable statements
 that identify the ability or performance of student learning outcomes accompanied by evidence.
- 6. Assessment Criteria are benchmarks used as a measure or measure of learning achievement in assessments based on predetermined indicators. Assessment criteria are guidelines for assessors so that assessments are consistent and unbiased. Criteria can be quantitative or qualitative.
- 7. Forms of assessment: test and non-test.
- 8. Forms of learning: Lecture, Response, Tutorial, Seminar or equivalent, Practicum, Studio Practice, Workshop Practice, Field Practice, Research, Community Service and/or other equivalent forms of learning.
- Learning Methods: Small Group Discussion, Role-Play & Simulation, Discovery Learning, Self-Directed Learning, Cooperative Learning, Collaborative Learning, Contextual Learning, Project Based Learning, and other equivalent methods.
- 10. Learning materials are details or descriptions of study materials which can be presented in the form of several main points and sub-topics.
- 11. The assessment weight is the percentage of assessment of each sub-PO achievement whose size is proportional to the level of difficulty of achieving that sub-PO, and the total is 100%.
- 12. TM=Face to face, PT=Structured assignments, BM=Independent study.