

Universitas Negeri Surabaya Faculty of Engineering, Undergraduate Study Program in Informatics Engineering

Document Code

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SEMESTER LEARNING PLAN

| Courses | | | | CODE | CODE Cou | | | urse | se Family Credit Weight | | | | | S | EMES | TER | Compila | ation | | |
|------------------------------|---|---|---|--|-------------------------|-----------------------------|--------------------------------|----------------------|-------------------------|-------------------------|---|-------------------------|---------------------|---------------------------|---|-------------------------------|----------------------------------|----------------------------------|--|-------------------------|
| Compute Organiza | er Arc | chitecture and | | 552020300 | 6 | | | Cor Pro | mpuls | sory S Subj | tudy ects | Т | =3 | P=0 | ECTS=4. | 77 | 2 | 2 | July 17, | 2024 |
| AUTHOR | IZAT | ION | | SP Developer | | | | | Cou | irse (| Clus | ter C | oordinato | r S | Study F | Program | Coordina | tor | | |
| | | | | Aditya Prapanca, S.T., M.Kom. | | | | | | Adit | Aditya Prapanca, S.T., M.Kom. | | | | m. / | Aditya Prapanca, S.T., M.Kom. | | Kom. | | |
| Learning model | | Project Based Lo | earning | 9 | | | | | | | | | | | | | | | | |
| Program | m PLO study program that is charged to the course | | | | | | | | | | | | | | | | | | | |
| Learning Outcome (PLO) |) es | PLO-2 | Able to societ | o design and y using theo | l simu retica | ılate m I conc | nulti-pla epts in | atfor the | m teo field | chnolo of cor | gy ap npute | plica r scie | tion | s that /inforr | are releva natics kno | nt to t wledg | the nee ge (KN0 | eds of ind O-02) | lustry and | |
| | | PLO-4 | Have | the ability to | work | in a te | eam (S | KI-C |)1) | | | | | | | | | | | |
| | | Program Objec | tives (| PO) | | | | | | | | | | | | | | | | |
| | | PO - 1 | 2Stude | ents are able | e to de | escribe | e the a | rchit | tectur | e and | orgar | nizati | on c | f the p | processor | (CPU |) on a | compute | r. | |
| | | PO - 2 | Studer | nts are able t | to des | scribe | the arc | chite | ecture | and | organi | zatio | n of | comp | uter syste | ns | | | | |
| | | PLO-PO Matrix | | | | | | | | | | | | | | | | | | |
| | | | | P.0 | | PLC | D-2 | | F | PLO-4 | | | | | | | | | | |
| | | | | PO-1 | | | | | | | | | | | | | | | | |
| | | | - | PO-2 | | | | | | | | | | | | | | | | |
| | | | | | | | | | | | |] | | | | | | | | |
| | | PO Matrix at the | e end o | of each lea | rning | y stag | je (Su | b-P | 0) | | | | | | | | | | | |
| | | | | | 1 | | | | | | | | | | | | | | | _ |
| | | | | P.0 | | | | | 1 | 1 | | | W | eek | | | | | | _ |
| | | | | | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 0 11 | 12 | 13 | 14 | 15 16 | |
| | | | PO |)-1 | | | | | | | | | | | | | | | | |
| | | | PO |)-2 | | | | | | | | | | | | | | | | |
| Short Course Descript | tion | This course teac computer system role of each com Control Unit Oper | hes ab s incluc ponent ations. | out modern ding Bus Sys t that makes | comp stems s up (| outer a , Inter compu | archite rnal an ıting sı | cture d Ei uch | e and xterna as C | l orga al Me ompu | nizati nory a ter Ar | on co and I ithme | omp npu etic, | rehen t/Outp Instru | sively by out. Furthe uction Set, | emph rmore CPL | nasizing e, this d J Struc | g the ba course s ture and | sic concep tudies the I Function | ots of main , and |
| Reference | ces | Main : | | | | | | | | | | | | | | | | | | |
| | | Stalling, Williams. 2010. Computer Organization and Architecture: Designing for Performance Eighth Edition. United States: Pearson Prentice-Hall. Carl Hamacher, Zvonko Vranesic dan Safwat Zaky. 2012. Computer Organization and Embedded Systems Sixth Edition. McGraw-Hill. John L Hennessy dan David Patterson. 2012. Computer Architecture A Quantitative Approach. Morgan Kaufman Tanenbaum, Andrew S. 2007. Structured Computer Organization. India: Prentice-Hall India. | | | | | | | | | | | | | | | | | | |
| | Supporters: | | | | | | | | | | | | | | | | | | | |
| | | 1. John L H | enness | y dan David | Patte | erson. | 2012. | Con | npute | r Arch | itectu | re A (| Qua | ntitativ | ve Approa | ch. M | organ I | Kaufmar | I | |
| Supporti lecturer | ing | Aditya Prapanca, I Made Suartana, Ronggo Alit, M.M | S.T., N S.Kom ., M.T. | I.Kom. I., M.Kom. | | | | | | | | | | | | | | | | |
| Week- | Fina eac | al abilities of h learning | | Ev | aluat | ion | | | | | Help Learning, Learning methods, Student Assignments, [Estimated time] | | | | | | Lear mate | rning erials | Assess Weight | ment t (%) |

| | stage (Sub-PO) | Indicator | Criteria & Form | Offline (Online (online) offline) | | [References] | |
|-----|---|---|--|---|---|---|-----|
| (1) | (2) | (3) | (4) | (5) | (6) | (7) | (8) |
| 1 | Students are able to identify the functions of computer systems and the history of computer development | Explain the terms in computer systems Details the functions of a computer system Describe the structure of a computer system Examines the evolution of computer systems | Criteria: Cognitive Values, Character Values, and Psychomotor Values Form of Assessment : Participatory Activities | Scientific approach, presentation, question and answer, discussion and problem- based learning 2 X 50 | Scientific approach, presentation, question and answer, discussion, and problem-based learning 50 | Material: computer systems References: Stalling, Williams. 2010. Computer Organization and Architecture: Designing for Performance Eighth Edition. United States: Pearson Prentice Hall. | 5% |
| 2 | Students are able to formulate the components of a computer system | Identify the concept of Von Neumann computer components Examining the basic components of a computer system | Criteria: Cognitive Values, Character Values, and Psychomotor Values Form of Assessment : Participatory Activities | Scientific approach, presentation, question and answer, discussion and problem- based learning 2 X 50 | Scientific approach, presentation, question and answer, discussion, and problem-based learning 50 | Material: Von Neumann's concept of computer components References: Stalling, Williams. 2010. Computer Organization and Architecture: Designing for Performance Eighth Edition. United States: Pearson Prentice Hall. | 5% |
| 3 | Students are able to apply Arithmetic and Logic operations. | Performing calculations with arithmetic operations, addition of multiplication in binary Perform calculations of logical operations. | Criteria: Cognitive Values, Character Values, and Psychomotor Values Form of Assessment : Participatory Activities | Scientific approach, presentation, question and answer, discussion and problem- based learning 2 X 50 | Scientific approach, presentation, question and answer, discussion and problem-based learning 1 x 50 | Material: calculations using arithmetic operations Reference: Tanenbaum, Andrew S. 2007. Structured Computer Organization. India : Prentice- Hall India. | 5% |
| 4 | Students are able to identify the working principles of the memory system in a computer. | Explain the different types of memory in a computer system Explain the working system of internal memory technology Explain the working system of external memory technology Diagram the memory addressing process | Criteria: Cognitive Values, Character Values, and Psychomotor Values Form of Assessment : Participatory Activities | Scientific approach, presentation, question and answer, discussion and problem- based learning 4 X 50 | Scientific approach, presentation, question and answer, discussion and problem-based learning 1 x 50 | Material: types of memory in computer systems Reference: Stalling, Williams. 2010. Computer Organization and Architecture: Designing for Performance Eighth Edition. United States: Pearson Prentice Hall. | 5% |
| 5 | | characteristics of a computer's Instruction Set | Criteria: Cognitive Values, Character Values, and Psychomotor Values Form of Assessment : Participatory Activities | Scientific approach, presentation, question and answer, discussion and problem- based learning 2 x 50 | Scientific approach, presentation, question and answer, discussion, and problem-based learning 1x 50 | Material: characteristics of computer Instruction Sets References: Stalling, Williams. 2010. Computer Organization and Architecture: Designing for Performance Eighth Edition. United States: Pearson Prentice Hall. | 5% |

| 6 | Students are able to explain the CPU work process | Details the characteristics of a computer's Instruction Set Details the function of a computer's instruction set Explain the principles of machine instructions | Criteria: Cognitive Values, Character Values, and Psychomotor Values Form of Assessment : Participatory Activities | Scientific approach, presentation, question and answer, discussion and problem- based learning 4 X 50 | Scientific approach, presentation, question and answer, discussion and problem-based learning | Material: characteristics of computer Instruction Sets References: Stalling, Williams. 2010. Computer Organization and Architecture: Designing for Performance Eighth Edition. United States: Pearson Prentice Hall. | 0% |
|----|--|---|--|---|---|---|----|
| 7 | Students are able to explain the CPU work process | Details the characteristics of a computer's Instruction Set Details the function of a computer's instruction set Explain the principles of machine instructions | Criteria: Cognitive Values, Character Values, and Psychomotor Values Form of Assessment : Participatory Activities | Scientific approach, presentation, question and answer, discussion and problem- based learning 4 X 50 | Scientific approach, presentation, question and answer, discussion and problem-based learning | Materials: Computer Instruction Set Bibliography: Stalling, Williams. 2010. Computer Organization and Architecture: Designing for Performance Eighth Edition. United States: Pearson Prentice Hall. | 5% |
| 8 | Students are able to explain the CPU work process | Details the characteristics of a computer's Instruction Set Details the function of a computer's instruction set Explain the principles of machine instructions | | Scientific approach, presentation, question and answer, discussion and problem- based learning 4 X 50 | Scientific approach, presentation, question and answer, discussion and problem-based learning | Materials: Computer Instruction Set Bibliography: Stalling, Williams. 2010. Computer Organization and Architecture: Designing for Performance Eighth Edition. United States: Pearson Prentice Hall. | 0% |
| 9 | Students are able to understand how I/O and DMA work in computers | Identify I/O modules Explain how Direct Memory Access works Explain the concept of I/O Channels and Processors Explain the Complex Instruction process | Criteria: Cognitive Values, Character Values, and Psychomotor Values | Scientific approach, presentation, question and answer, discussion and problem- based learning 4 X 50 | Scientific approach, presentation, question and answer, discussion and problem-based learning | Material: Identifying I/O modules References: Stalling, Williams. 2010. Computer Organization and Architecture: Designing for Performance Eighth Edition. United States: Pearson Prentice Hall. | 0% |
| 10 | Students are able to understand how I/O and DMA work in computers | Identify I/O modules Explain how Direct Memory Access works Explain the concept of I/O Channels and Processors Explain the Complex Instruction process | Criteria: Cognitive Values, Character Values, and Psychomotor Values | Scientific approach, presentation, question and answer, discussion and problem- based learning 4 X 50 | Scientific approach, presentation, question and answer, discussion and problem-based learning | Material: Identifying I/O modules References: Stalling, Williams. 2010. Computer Organization and Architecture: Designing for Performance Eighth Edition. United States: Pearson Prentice Hall. | 0% |

| 11 | Students are able to understand the working principles of Computer Interconnection Structures | Explains how Structure Interconnection works Describes the transfer process between memory, I/O, CPU Explain the working concept of PCI Bus | Criteria: Cognitive Values, Character Values, and Psychomotor Values | Scientific approach, presentation, question and answer, discussion and problem- based learning 2 X 50 | Scientific approach, presentation, question and answer, discussion and problem-based learning | Material: how structural interconnections work Library: Stalling, Williams. 2010. Computer Organization and Architecture: Designing for Performance Eighth Edition. United States: Pearson Prentice Hall. | 0% |
|----|---|--|--|---|---|---|-----|
| 12 | Students understand the concept and function of Reduced Instruction Set Computer (RISC) | 1.Describes the RISC process in computer architecture 2.Identify the uses of RISC in modern computer architecture | Criteria: Cognitive Values, Character Values, and Psychomotor Values | Scientific approach, presentation, question and answer, discussion and problem- based learning 2 X 50 | Scientific approach, presentation, question and answer, discussion and problem-based learning | Material: RISC in modern computer architecture Reference: Tanenbaum, Andrew S. 2007. Structured Computer Organization. India : Prentice- Hall India. | 0% |
| 13 | Students understand the concept and function of pipelines. | Students can explain the concept and function of pipelines Distinguishing processor performance from pipelines | Criteria: Cognitive Values, Character Values, and Psychomotor Values | Scientific approach, presentation, question and answer, discussion and problem- based learning 2 X 50 | Scientific approach, presentation, question and answer, discussion and problem-based learning | Material: pipeline concepts and functions References: Stalling, Williams. 2010. Computer Organization and Architecture: Designing for Performance Eighth Edition. United States: Pearson Prentice Hall. | 0% |
| 14 | Students are able to understand the concepts of multi- processor and parallel processing | Can explain the concept of multiprocessing Distinguish between single processor and multi processor a.Mention the advantages of multiprocessors Explain the concept of parallel processing | Criteria: Cognitive Values, Character Values, and Psychomotor Values Form of Assessment : Participatory Activities | Scientific approach, presentation, question and answer, discussion and problem- based learning 2 X 50 | Scientific approach, presentation, question and answer, discussion and problem-based learning | Material: process between single processor and multi processor Reference: Stalling, Williams. 2010. Computer Organization and Architecture: Designing for Performance Eighth Edition. United States: Pearson Prentice Hall. | 15% |
| 15 | Students are able to analyze the application of computer architecture concepts with case studies of the Intel 8085 and Intel 8086 microprocessors. | Linking architectural concepts and implementation in organizations | Criteria: Cognitive Values, Character Values, and Psychomotor Values Form of Assessment : Project Results Assessment / Product Assessment | Scientific approach, presentation, question and answer, discussion and problem- based learning 2 X 50 | Scientific approach, presentation, question and answer, discussion and problem-based learning | Material: architectural concepts and application in organizations References: Stalling, Williams. 2010. Computer Organization and Architecture: Designing for Performance Eighth Edition. United States: Pearson Prentice Hall. | 55% |

| 16 | Students are able to analyze the application of computer architecture concepts with case studies of the Intel 8085 and Intel 8086 microprocessors. | Linking architectural concepts and implementation in organizations | Criteria: Cognitive Values, Character Values, and Psychomotor Values Form of Assessment : Project Results Assessment / Product Assessment | Scientific approach, presentation, question and answer, discussion and problem- based learning 2 X 50 | Scientific approach, presentation, question and answer, discussion and problem-based learning | Material: architectural concepts and application in organizations References: Stalling, Williams. 2010. Computer Organization and Architecture: Designing for Performance Eighth Edition. United States: Pearson Prentice Hall. | 0% |
|----|---|--|--|---|---|---|----|
|----|---|--|--|---|---|---|----|

Evaluation Percentage Recap: Project Based Learning

| No | Evaluation | Percentage |
|----|---|------------|
| 1. | Participatory Activities | 45% |
| 2. | Project Results Assessment / Product Assessment | 55% |
| | | 100% |

Notes

- Learning Outcomes of Study Program Graduates (PLO Study Program) are the abilities possessed by each Study
 Program graduate which are the internalization of attitudes, mastery of knowledge and skills according to the level of their
 study program obtained through the learning process.
- 2. The PLO imposed on courses are several learning outcomes of study program graduates (CPL-Study Program) which are used for the formation/development of a course consisting of aspects of attitude, general skills, special skills and knowledge.
- 3. **Program Objectives (PO)** are abilities that are specifically described from the PLO assigned to a course, and are specific to the study material or learning materials for that course.
- 4. **Subject Sub-PO (Sub-PO)** is a capability that is specifically described from the PO that can be measured or observed and is the final ability that is planned at each learning stage, and is specific to the learning material of the course.
- 5. Indicators for assessing abilities in the process and student learning outcomes are specific and measurable statements that identify the abilities or performance of student learning outcomes accompanied by evidence.
- 6. Assessment Criteria are benchmarks used as a measure or measure of learning achievement in assessments based on predetermined indicators. Assessment criteria are guidelines for assessors so that assessments are consistent and unbiased. Criteria can be quantitative or qualitative.
- 7. Forms of assessment: test and non-test.
- 8. Forms of learning: Lecture, Response, Tutorial, Seminar or equivalent, Practicum, Studio Practice, Workshop Practice, Field Practice, Research, Community Service and/or other equivalent forms of learning.
- 9. Learning Methods: Small Group Discussion, Role-Play & Simulation, Discovery Learning, Self-Directed Learning,
- Cooperative Learning, Collaborative Learning, Contextual Learning, Project Based Learning, and other equivalent methods. 10. Learning materials are details or descriptions of study materials which can be presented in the form of several main points and sub-topics.
- 11. The assessment weight is the percentage of assessment of each sub-PO achievement whose size is proportional to the level of difficulty of achieving that sub-PO, and the total is 100%.
- 12. TM=Face to face, PT=Structured assignments, BM=Independent study.