

Universitas Negeri Surabaya Faculty of Engineering , Electrical Engineering Education Undergraduate Study Program

Document Code

SEMESTER LEARNING PLAN

| Courses | | | CODE | | Course Fa | mily | Credit W | /eight | SE | MESTER | Compilation Date | |
|--------------------------------|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------|---------------|---------------|---------------------------------------------------------------------------------|----------------|----------|--------------|------------------------------------|----------------------------|-------|
| Microprocessor | | | 8320103079 | | | | T=3 P= | 0 ECTS=4 | .77 | 6 | July 18, 2024 | |
| AUTHORIZATION | | | | SP Developer | | Course Cluster Coordinator | | | r Stu Co | Study Program Coordinator | | |
| | | | | | | | | | | D | Dr. Nur Kholis, S.T., M.T. | |
| Learning model | I | Case Studies | | | | | | | | | | |
| Program | 1 | PLO study program that is charged to the course | | | | | | | | | | |
| Outcom | g es | Program Objectives (PO) | | | | | | | | | | |
| (PLO) | | PLO-PO Matrix | | | | | | | | | | |
| | | P.O | | | | | | | | | | |
| | | PO Matrix at the end of each learning stage (Sub-PO) | | | | | | | | | | |
| | | | | | | | | | | | | |
| | | | P.0 |) Week | | | | | | | | |
| | | | | 1 2 | 3 4 5 | 6 7 | 8 | 9 10 | 11 12 | 13 | 14 | 15 16 |
| | | | | | | 1 1 | II | 1 | 1 1 | | - L - L - L | |
| Short Course Description | | This course equips electrical engineering education students to understand how microprocessors work on computers. The material that will be studied in this course includes computer architecture, a minimum 8088 microprocessor system and microprocessor programming techniques using assembly language. After taking this course, students are expected to be able to explain the definition and how a microprocessor works on a computer, starting from architecture, programming and interfaces. | | | | | | | | | | |
| References | | Main : | | | | | | | | | | |
| | | Brey, Barry B. 2003. Mikroprosesor Intel, 6th Edition. New Jersey: The Pearson Education, Inc. Setiawan, Rachmad. 2006. Mikroprosesor 8088. Yogyakarta: Graha Ilmu. | | | | | | | | | | |
| | | Supporters: | | | | | | | | | | |
| | | | | | | | | | | | | |
| Support lecturer | ing | Adam Ridiantho I Arif Widodo, S.T. | Muhama , M.Sc. | d, S.T., M.T. | | | | | | | | |
| Week- | Fin eac stag | Final abilities of each learning stage (Sub-PO) | | Evaluat | | Help Learning, Learning methods, Student Assignments, [Estimated time] | | | L m Re | earning naterials [weigi | Assessment Weight (%) | |
| | JSu | | | ndicator | Criteria & Fo | orm Off | line(line) | Onlin | e (online) | | 1 | |
| (1) | | (2) | | (3) | (4) | | (5) | | (6) | | (7) | (8) |

| 1 | Explain the definition and development of microprocessors | Explain the definition of a microprocessor and its differences from a microcontroller Tells the history of microprocessors Mention several types of microprocessors and their development Mention the applications of microprocessors in everyday life | Model: Discovery learning Method: Discussion Approach: Scientific 3 X 50 | | 0% |
|---|--------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------------------------------------------------|--|----|
| 2 | Explain microprocessor architecture | Understand the internal architecture of a microprocessor Mention 3 types of microprocessor architecture Classifying microprocessor architecture based on the number of instruction sets | Model: Discovery learning Method: Discussion Approach: Scientific 3 X 50 | | 0% |
| 3 | Explain microprocessor architecture | Understand the internal architecture of a microprocessor Mention 3 types of microprocessor architecture Classifying microprocessor architecture based on the number of instruction sets | Model: Discovery learning Method: Discussion Approach: Scientific 3 X 50 | | 0% |
| 4 | Students are able to analyze how microprocessors work | Understanding the Bus System Architecture on the 8088 microprocessor Mentions Addressing Modes Shows how the microprocessor interfaces with other components | Model: Discovery/ Inquiry Method: Discussion Approach: Constructivist 3 X 50 | | 0% |
| 5 | Students are able to analyze how microprocessors work | Understanding the Bus System Architecture on the 8088 microprocessor Mentions Addressing Modes Shows how the microprocessor interfaces with other components | Model: Discovery/ Inquiry Method: Discussion Approach: Constructivist 3 X 50 | | 0% |
| 6 | | | | | 0% |
| 7 | | | | | 0% |
| 8 | | | | | 0% |

| 9 | | | | 0% |
|----|--|--|--|----|
| 10 | | | | 0% |
| 11 | | | | 0% |
| 12 | | | | 0% |
| 13 | | | | 0% |
| 14 | | | | 0% |
| 15 | | | | 0% |
| 16 | | | | 0% |

Evaluation Percentage Recap: Case Study

No Evaluation Percentage

Notes

- Learning Outcomes of Study Program Graduates (PLO Study Program) are the abilities possessed by each Study Program graduate which are the internalization of attitudes, mastery of knowledge and skills according to the level of their study program obtained through the learning process.
- The PLO imposed on courses are several learning outcomes of study program graduates (CPL-Study Program) which are used for the formation/development of a course consisting of aspects of attitude, general skills, special skills and knowledge.
- 3. **Program Objectives (PO)** are abilities that are specifically described from the PLO assigned to a course, and are specific to the study material or learning materials for that course.
- 4. **Subject Sub-PO (Sub-PO)** is a capability that is specifically described from the PO that can be measured or observed and is the final ability that is planned at each learning stage, and is specific to the learning material of the course.
- 5. Indicators for assessing ability in the process and student learning outcomes are specific and measurable statements that identify the ability or performance of student learning outcomes accompanied by evidence.
- 6. Assessment Criteria are benchmarks used as a measure or measure of learning achievement in assessments based on predetermined indicators. Assessment criteria are guidelines for assessors so that assessments are consistent and unbiased. Criteria can be quantitative or qualitative.
- 7. Forms of assessment: test and non-test.
- 8. Forms of learning: Lecture, Response, Tutorial, Seminar or equivalent, Practicum, Studio Practice, Workshop Practice, Field Practice, Research, Community Service and/or other equivalent forms of learning.
- Learning Methods: Small Group Discussion, Role-Play & Simulation, Discovery Learning, Self-Directed Learning, Cooperative Learning, Collaborative Learning, Contextual Learning, Project Based Learning, and other equivalent methods.
- **10. Learning materials** are details or descriptions of study materials which can be presented in the form of several main points and sub-topics.
- **11. The assessment weight** is the percentage of assessment of each sub-PO achievement whose size is proportional to the level of difficulty of achieving that sub-PO, and the total is 100%.
- 12. TM=Face to face, PT=Structured assignments, BM=Independent study.