

Universitas Negeri Surabaya Faculty of Engineering , Electrical Engineering Education Undergraduate Study Program

Document Code

SEMESTER LEARNING PLAN

Courses			CODE	(Course Famil	y C	Credit Wei	ght	SEMESTER	Compilation Date		
Advanced Electronic Circuits			832010225	5		Т	Г=2 Р=0	ECTS=3.18	4	July 17, 2024		
AUTHORIZATION			SP Develo	SP Developer		Course Cluster Coordinator			Study Program Coordinator			
								Dr. Nur Kholis, S.T., M.T.				
Learning model	I	Case Studies										
Program		PLO study program that is charged to the course										
Learning Outcomes (PLO)		Program Objectives (PO)										
		PLO-PO Matrix										
		P.O										
		PO Matrix at the end of each learning stage (Sub-PO)										
			P.O Week 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16									
Short Course Description												
References		Main :										
		 A.P Malvino 1993, Elektronic prinsiples. singapore : Mcgraw-hillSchults, ME 1994. Electronic Devices.Singapore:Glencoe 										
		Supporters:										
Support lecturer	pporting Dr. Nur Kholis, S.T., M.T.											
week ea		al abilities of h learning ge	Evaluation			Help Learning, Learning methods, Student Assignments, [Estimated time]		Learning materials References	Assessment Weight (%)			
	(Sub-PO)		Indicator	Criteria & Fo	orm Offl	ine (ine)	Online	(online)]			
(1)		(2)	(3)	(4)	(5)	(6)	(7)	(8)		
the concept of FET c components o E		- Explain the characteristics of FET - Explain how to provide FET bias.	Criteria: student activi in learning		ion				0%			

				-		
2	Students are able to analyze FET circuits as amplifiers.	 Explain the meaning and function of common source, common drain, and common gate amplifiers. Calculating the value of electrical quantities in the FET circuit as an amplifier. 	Criteria: competency in analyzing questions	Presentation, discussion and practice 2 X 50		0%
3	Students are able to analyze FET circuits as amplifiers.	 Explain the meaning and function of common source, common drain, and common gate amplifiers. Calculating the value of electrical quantities in the FET circuit as an amplifier. 	Criteria: competency in analyzing questions	Presentation, discussion and practice 2 X 50		0%
4	Students understand the concept of Thyristors (DIAC, SCR, and TRIAC) which includes their characteristics and functions	- Explain the characteristics and function of DIAC - Explain the characteristics and function of SCR - Explain the characteristics and function of TRIAC	Criteria: student activities in learning	Presentation, discussion and practice 2 X 50		0%
5	Students understand the concept of Thyristors (DIAC, SCR, and TRIAC) which includes their characteristics and functions	- Explain the characteristics and function of DIAC - Explain the characteristics and function of SCR - Explain the characteristics and function of TRIAC	Criteria: student activities in learning	Presentation, discussion and practice 2 X 50		0%
6	Students are able to analyze the influence of RF on electronic circuits	- Explain RF Transistor Amplifier - Explain RF Amplifier FET.	Criteria: ability to solve problems	Discussion, exercises and assignments 2 X 50		0%
7	Students are able to analyze the influence of RF on electronic circuits	- Explain RF Transistor Amplifier - Explain RF Amplifier FET.	Criteria: ability to solve problems	Discussion, exercises and assignments 2 X 50		0%
8	UTS	UTS	Criteria: UTS	UTS 2 X 50		0%
9						0%

10				0%
11				0%
12				0%
13				0%
14				0%
15				0%
16				0%

 Evaluation Percentage Recap: Case Study

 No
 Evaluation

 Percentage

 0%

Notes

- Learning Outcomes of Study Program Graduates (PLO Study Program) are the abilities possessed by each Study Program graduate which are the internalization of attitudes, mastery of knowledge and skills according to the level of their study program obtained through the learning process.
- The PLO imposed on courses are several learning outcomes of study program graduates (CPL-Study Program) which are used for the formation/development of a course consisting of aspects of attitude, general skills, special skills and knowledge.
- 3. **Program Objectives (PO)** are abilities that are specifically described from the PLO assigned to a course, and are specific to the study material or learning materials for that course.
- 4. **Subject Sub-PO (Sub-PO)** is a capability that is specifically described from the PO that can be measured or observed and is the final ability that is planned at each learning stage, and is specific to the learning material of the course.
- 5. Indicators for assessing abilities in the process and student learning outcomes are specific and measurable statements that identify the abilities or performance of student learning outcomes accompanied by evidence.
- 6. Assessment Criteria are benchmarks used as a measure or measure of learning achievement in assessments based on predetermined indicators. Assessment criteria are guidelines for assessors so that assessments are consistent and unbiased. Criteria can be quantitative or qualitative.
- 7. Forms of assessment: test and non-test.
- 8. Forms of learning: Lecture, Response, Tutorial, Seminar or equivalent, Practicum, Studio Practice, Workshop Practice, Field Practice, Research, Community Service and/or other equivalent forms of learning.
- 9. Learning Methods: Small Group Discussion, Role-Play & Simulation, Discovery Learning, Self-Directed Learning, Cooperative Learning, Contextual Learning, Project Based Learning, and other equivalent methods.
- 10. Learning materials are details or descriptions of study materials which can be presented in the form of several main points and sub-topics.
- 11. The assessment weight is the percentage of assessment of each sub-PO achievement whose size is proportional to the level of difficulty of achieving that sub-PO, and the total is 100%.
- 12. TM=Face to face, PT=Structured assignments, BM=Independent study.